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**PATENT** 

Case Docket No. ASMEX.320A

Date: May 6, 2003

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Pomarede et al.

Appl. No.

10/074,722

Filed

February 11, 2002

For

INTEGRATION OF HIGH K

GATE DIELECTRIC

Examiner

Ron Everett Pompey

Group Art Unit:

2812

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

May 6, 2003

Joseph J. Malon, Reg. No. 39,287

#### TRANSMITTAL LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

#### Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
- (X) A PTO Form 1449 with one (1) references.
- (X) A check in the amount of \$180 to cover the above fee in enclosed.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

Joseph J. Mallon

Regist ation No. 39,287

Attorney of Record

Customer No. 20,995

(619) ::35-8550

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# **Knobbe Martens Olson & Bear LLP**

Intellectual Property Law

550 West C Street Suite 1200 San Diego CA 92101 Tel 619-235-8550 Fax 619-235-0176 www.kmob.com

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Examiner Ron Everett Pompey

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USPTO

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OUR REF.:

ASMEX.320A

YOUR REF.:

10/074,722

FROM:

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OPERATOR:

Susan Olson

No. Of Pages: 18

(incl. cover sheet)

DATE:

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Customer No.: 20,995

## AMENDMENT / RESPONSE T RANSMITTAL

Applicant

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Examiner

Pompey, Ron Everett

Art Unit

2812

872-9318 of the USPTO on the date shown

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Transmitted herewith for filing in the above-identified application are the following enclosures:

(X) Response in 6 pages.

(X) Courtesy copies of IDS and PTO Form 1449 filed on August 27, 2002 and May 6, 2003 along with copies of return postcards showing receipt by the USPTO on September 3, 2002 and May 9, 2003, respectively.

The fee has been calculated as shown below:

					FEE	CALCULATION		
FEE TYPE						FEE CODE	CALCULATION	TOTAL
Total Claims	47	-	47	=:	0	1202 (\$18)	0 x 18 =	\$0
Independent Claims	3	-	3	=-	0	1201 (\$84)	0 x 84 =	\$0
Multiple Claim						1203 (\$280)		\$0
1 Month Extension						1251 (\$110)		\$0
	<b>-</b> -						TOTAL FEE DUE	SO

(X) Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Joseph J. Mallon

Registration No. 39,287

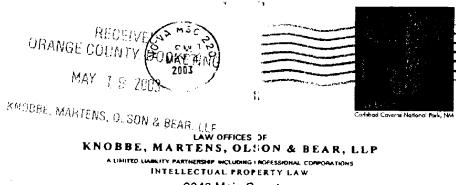
Attorney of Record

Customer No. 20,995

(619) 235-8550

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UTILITY/DESIGN PATENT (document recondation./ IDS)	Date: May 6, 2003
Rec'd in the USPTO on the date stamped hereon via Cartif	ficate of Mail:
Docket #: ASMEX.320 A Applicant:	
Title: INTEGRATION OF HIGH K GATE I	
App No.: 10 (074, 7)27 Filed:	February 11, 2002
	7812
Class/Sub-Class: 438/286.000 Re-Exam N.:	
VERIFIED BY: Asst: SLO Atty: M	QC: <b></b> //
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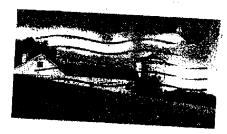
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UTILITY/DESIG PATIENT (document recordation IDS)	Date: May 200
Rec'd in the USPTO on the date stamped Docket #: ASME X. 376. A  Title: INTEGRATION OF HIGH App No.: 10 0 74. 722  EXI: BON LIPCOLT CIMPAL CLASS/Sub-Class: 438/2810.000  VERIFIED BY: Asst: SLO  Transmittal Letter  Information Disclosure Statement in 25 Form PTO-1449 in 1 pgs  4 \$180 Check for IDS	Applicant: Immarede et a U  K GATE DI ELECTRIC  Filed: F. Larrang 11, 2002  Art Unit: 2912  Re-Exam No.:  Atty: M. QC:
	Check for Recordation
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ÜTILITY/DESIGN PATENT (document revordation / IDS)	Date: August 27,2002
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UTILITY/DESIGN PATENT (document recordation / IDS)	Date: ()	ugust 27,200
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Title: IN 16 602 A T	licant: Pomo con	0 4 0
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**PATENT** 

Case Docket No. ASMEX.320A Date: August 27, 2002

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	:	Pomarede et al.	l hereby certify that this correspondence and all marked attachments are being deposited with the United States
Appl. No.	:	10/074,722	Postal Service as first class mail in an envelope addressed to: United States Patent and Trademark Office, P.O. Box 2327, Arlington, VA 22202, on
Filed	:	February 11, 2002	August 27, 2002
For	:	INTEGRATION OF HIGH K GATE DIELECTRIC	) Joseph J. Mallop, Reg. No. 39,287
Examiner	:	Unknown	)
Group Art Uni	it :	2812	, )

#### TRANSMITTAL LETTER

United States Patent and Trademark Office P.O. Box 2327 Arlington, VA 22202

ATTENTION: APPLICATION BRANCH

#### Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
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Josep 1 J. Mallon

Regis ration No. 39,287

Attorney of Record

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